

EL 844046303

EM025332499

DECLARATION OF JOINT INVENTORS FOR PATENT APPLICATION

As the below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and joint inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled: **Memory Integrated Circuitry**, the specification of which is attached hereto.

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims.

I acknowledge the duty to disclose information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations §1.56.

PRIOR FOREIGN APPLICATIONS:

I hereby state that no applications for foreign patents or inventor's certificates have been filed prior to the date of execution of this declaration.

POWER OF ATTORNEY:

As a named Inventor, I hereby appoint the following attorneys and agent to prosecute this application and transact all business in the Patent and Trademark Office connected therewith: Richard J. St. John, Reg. No. 19,363; David P. Roberts, Reg. No. 23,032; Randy A. Gregory, Reg. No. 30,386; Mark S. Matkin, Reg. No. 32,268; James L. Price, Reg. No. 27,376; Deepak Malhotra, Reg. No. 33,560; Mark W. Hendricksen,

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5 Michael L. Lynch, Reg. No. 30,871.

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8 WA 99204-0317. Direct telephone calls to: Mark S. Matkin,
9 (509) 624-4276.

10 I hereby declare that all statements made herein of my own
11 knowledge are true and that all statements made on information and
12 belief are believed to be true; and further that these statements were
13 made with the knowledge that willful false statements and the like so
14 made are punishable by fine or imprisonment, or both, under
15 Section 1001 of Title 18 of the United States Code and that such willful
16 false statement may jeopardize the validity of the application or any
17 patent issued therefrom.
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* * * * *

Full name of inventor: Luan Tran

Inventor's Signature: Luan Tran

Date: 4/6/97

Residence: Meridian, Idaho

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* * * * *

Full name of inventor: Alan R. Reinberg

Inventor's Signature: Alan R. Reinberg

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

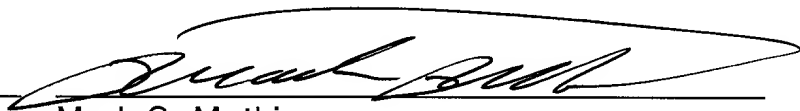
Priority Application Serial No. 08/842,230
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Inventor Luan Tran et al.
Assignee Micron Technology, Inc.
Priority Group Art Unit 2814
Priority Examiner H. Weiss
Attorney's Docket No. MI22-1784
Title: Memory Integrated Circuitry

Assistant Commissioner for Patents
Washington, D.C. 20231

ASSOCIATE POWER OF ATTORNEY

Please recognize Frederick M. Fliegel, Reg. No. 36,138; D. Brent Kenady, Reg. No. 40,045; James E. Lake, Reg. No. 44,854; Bernard Berman, Reg. No. 37,279; and Jennifer Taylor, Reg. No. P-48,711; whose post office address is 601 W. First Avenue, Suite 1300, Spokane, Washington 99201-3828, as associate attorneys or agents in the above-entitled application.

Date: 8-13-01



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